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**Southwell et al.**

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(54) **CACHE MEMORY OPERATION**

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(57) **ABSTRACT**

A cache memory comprises a fetch engine arranged to issue fetch requests for accessing data items from locations in a main memory identified by access addresses in a program being executed, a pre-fetch engine controlled to issue pre-fetch requests for speculatively accessing pre-fetch data items from locations in said main memory identified by addresses which are determined as being a number of locations from respective ones of said access addresses, and a calibrator arranged to selectively vary said number of locations.

**36 Claims, 3 Drawing Sheets**

